

Fin_p Fin_m Dp Dm DFF clkp clk_m

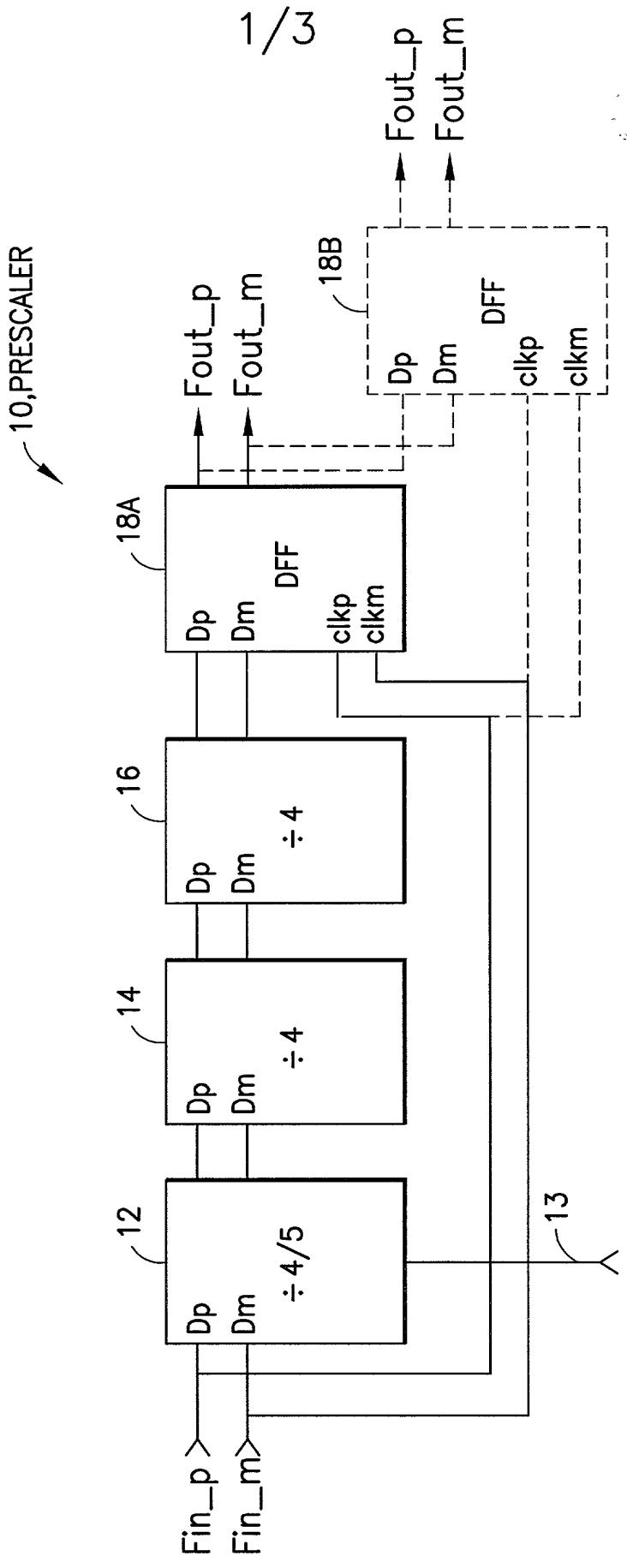


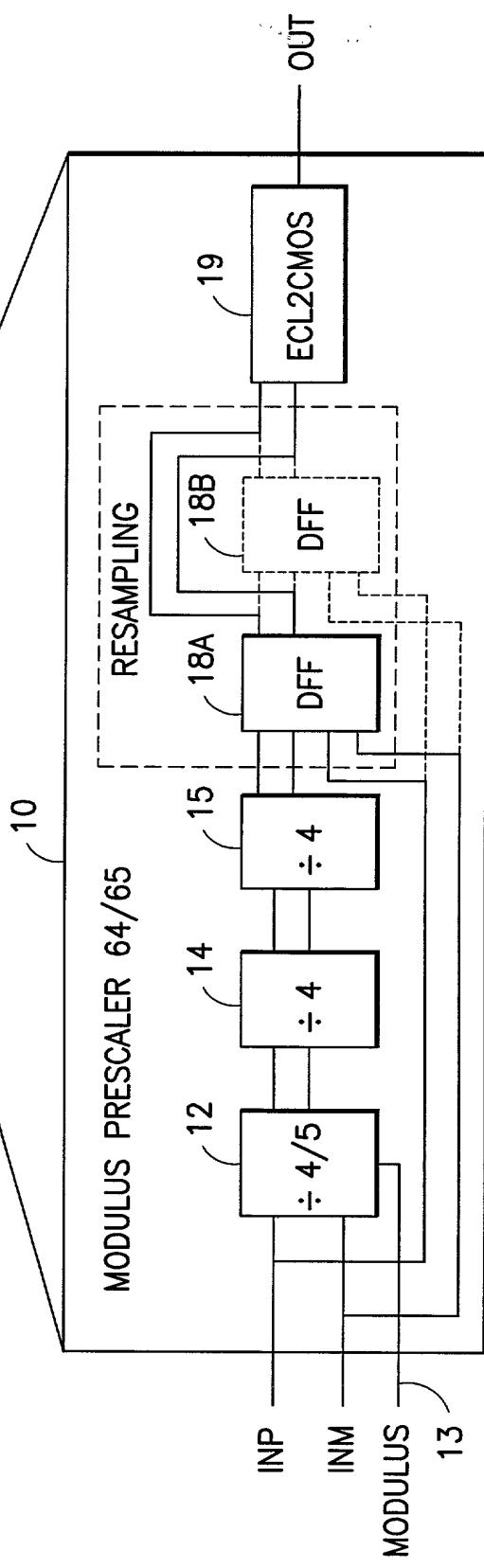
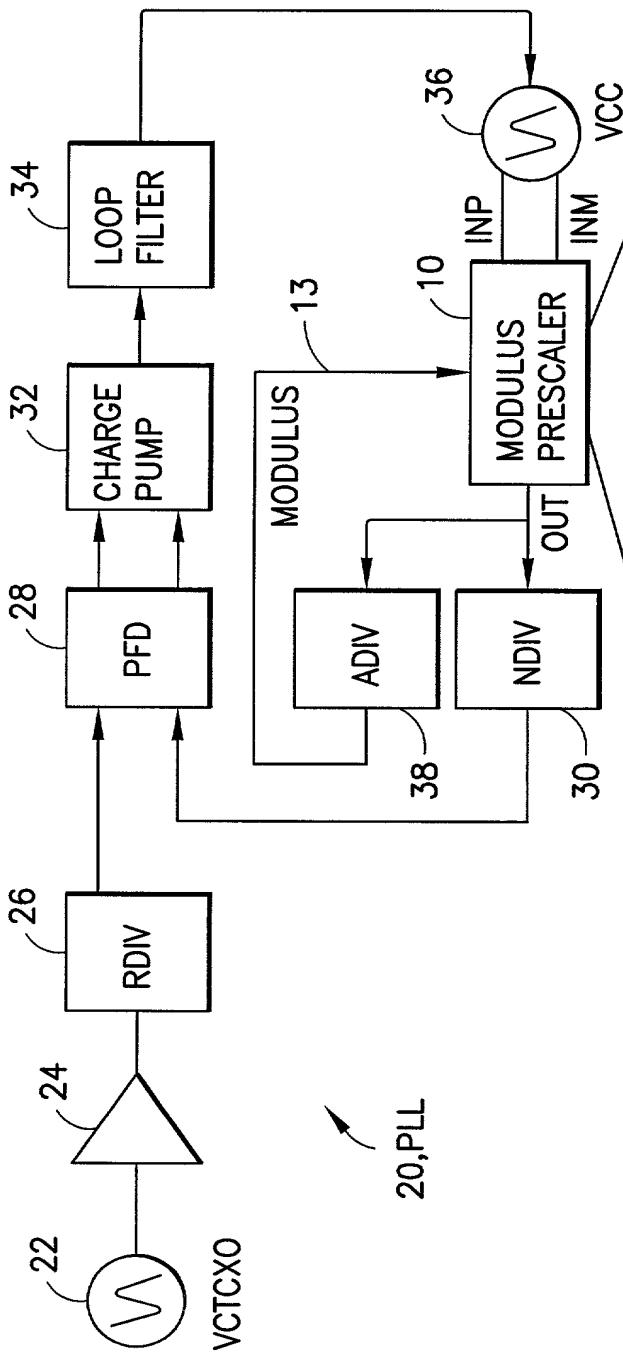
FIG.1

MODULUS CONTROL

FIG.2

2/3

22 24 26 28 32 34
VCTCXO



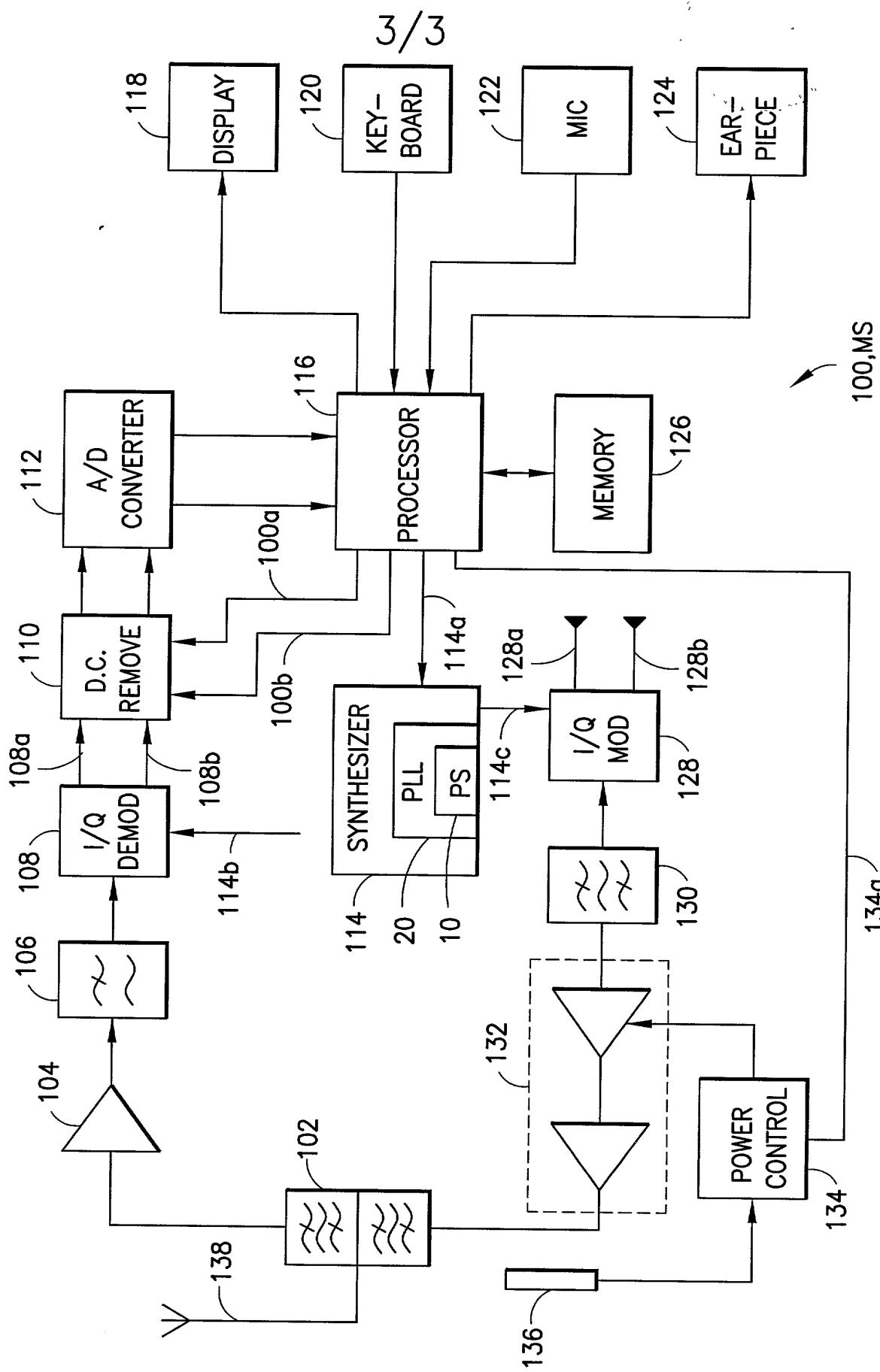


FIG. 3